	Application No.	Applicant(s)
Notice of Allowability	10/719,495	ALCOE ET AL.
	Examiner	Art Unit
	David L. Hogans	2813
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with the co (OR REMAINS) CLOSED in this apport or other appropriate communication GHTS. This application is subject to and MPEP 1308.	plication. If not included not be mailed in due course. THIS
1. This communication is responsive to the Amendment filed on September 15, 2004.		
2. ☑ The allowed claim(s) is/are <u>15-19,21-26 and 30-38</u> .		
3. 🔀 The drawings filed on <u>21 November 2003</u> are accepted by the Examiner.		
4.		
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date	6. Interview Summary Paper No./Mail Da 7. Examiner's Amend 8. Examiner's Statem 9. Other SUF	ite

Application/Control Number: 10/719,495

Art Unit: 2813

# **DETAILED ACTION**

This Office Action is in response to the Amendment filed on September 15, 2004.

### Status of Claims

Claims 15-19, 21-26 and 30-38 are pending. Claims 1-14, 20 and 27-29 are cancelled. Claims 30-38 are newly added.

# Allowable Subject Matter

- 1. Claims 15-19, 21-26 and 30-38 are allowed.
- 2. The following is an examiner's statement of reasons for allowance.

#### Claim 15

The prior art of record fails to teach or suggest, in combination with the other claimed features, wherein the first via is in direct mechanical and electrical contact with the first power plane and wherein the second via is in direct mechanical and electrical contact with the second power plane.

# Claim 22

The prior art of record fails to teach or suggest, in combination with the other claimed features, forming a plated through hole, forming a first pair of signal planes within the substrate, forming a first pair of power cores within the substrate, forming a second pair of signal planes within the substrate, and forming a second pair of power cores within the substrate, wherein the first and second pair of signal planes are controlled impedance circuitry.

Art Unit: 2813

Claim 23

The prior art of record fails to teach or suggest, in combination with the other claimed features, forming a plated through hole, forming a first pair of signal planes within the substrate, forming a first pair of power cores within the substrate, forming a second pair of signal planes within the substrate, and forming a second pair of power cores within the substrate, wherein the second pair of power cores are directly underneath and electrically connected to portions of the redistribution layer.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Conclusion

- 3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
- 4,710,854 to Yamada et al. teaches forming power layers and signal layers with a plated through hole formed through the substrate but fails to teach: wherein the vias are in direct mechanical and electrical contact with the power planes; wherein the first and second pair of signal planes are controlled impedance circuitry; and wherein the second pair of power cores are directly underneath and electrically connected to portions of the redistribution layer.

Application/Control Number: 10/719,495

Art Unit: 2813

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David L. Hogans whose telephone number is (571) 272-1691. The examiner can normally be reached on M-F (7:30-4:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

